

FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use several sheets if necessary)

ΑТ	TY.	DOCKET NO.	
	1-2	-0344 1115	

SERIAL NO. 10/670,062

APPLICANT Buchert et al.

FILING DATE September 24, 2003 GROUP 2193

		U.S. PATENT	DOCUMENTS				
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS		DATE IF
	3,535,694	10/1970	Anacker et al.				
	4,811,210	03/1989	Maculay et al.				
	4,872,131	10/1989	Kubota et al.				
*	4,942,608	07/1990	Shigehara ¹			-	
	5,179,531	01/1993	Yamaki				
	5,268,856	12/1993	Wilson	1			
	5,301,340	04/1994	Cook				
	5,974,435	10/1999	Abbott et al.				
	6,141,673	10/2000	Thayer et al.				
	6,298,366	10/2001	Gatherer et al.				
	6,317,770	11/2001	Lim et al.				
	6,334,176	12/2001	Scales III et al.				
	6,366,937	04/2002	Shridhar et al.				
	6,401,194	06/2002	Nguyen et al.				
	6,526,430	02/2003	Hung et al.				
	6,530,010	03/2003	Hung et al.				
	6,992,716	07/2005	Desai et al.				
		FOREIGN PATEN	IT DOCUMENTS				
EXAMINER INITIAL						TRANS	SLATION
IIIII/NL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	YES	NO
	0952516	10/1999	EP EP				
	1335299	08/2003	EP				

1 US 4,942,608 is equivalent to JP Application 63-205786				
EXAMINER	DATE CONSIDERED			

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EXAMINER						TRANS	SLATION
INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	YES	NO
	11306163	11/1999	JP ²				
	2001236496	08/2001	JP ³				
	2002202964	07/2002	JP ⁴				
	2002527808	08/2002	JP ⁵				
	00/22503	04/2000	WO				
·		OTHER DO	CUMENTS				
EXAMINER INITIAL	DESCRIPTION (Including Author, Title, Date, Pertinent Pages, Etc.)						
	Catthoor et al., "Efficient Microcoded Processor Design for Fixed Rate DFT and FFT1", Journal of VLSI Signal Processing Systems For Signal, Image, and Video Technology, Vol. 1, No. 4, April 1, 1990, pp. 287-306.						

⁵ Corresponds to WO 00/22503

EXAMINER	DATE CONSIDERED				

² Corresponds to EP 0952516

³ Corresponds to US 6,530,010 4 Corresponds to EP 1335299